

Fig. 1

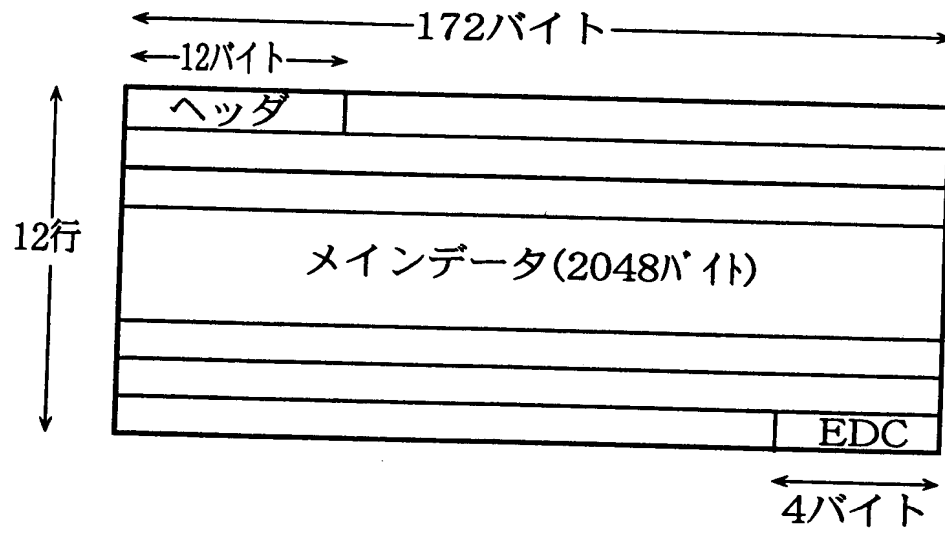


Fig. 2

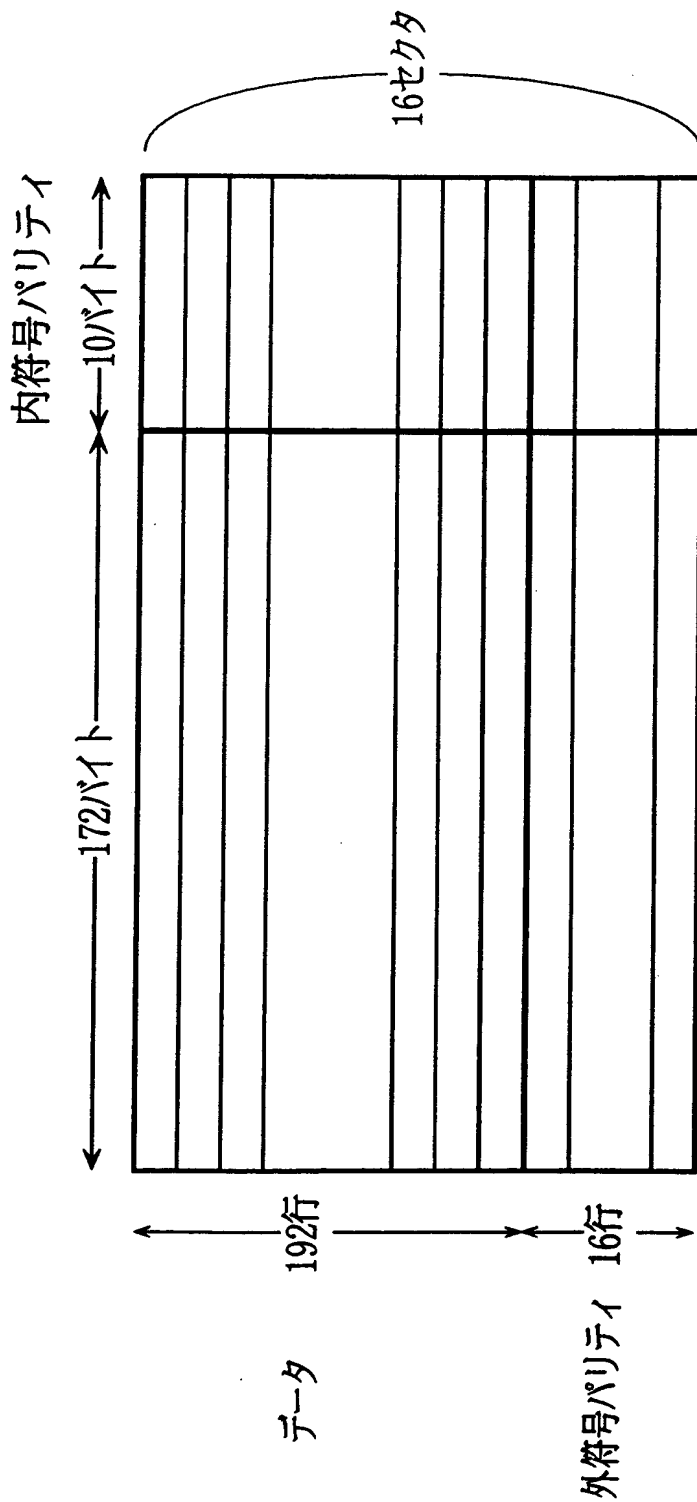


Fig. 3

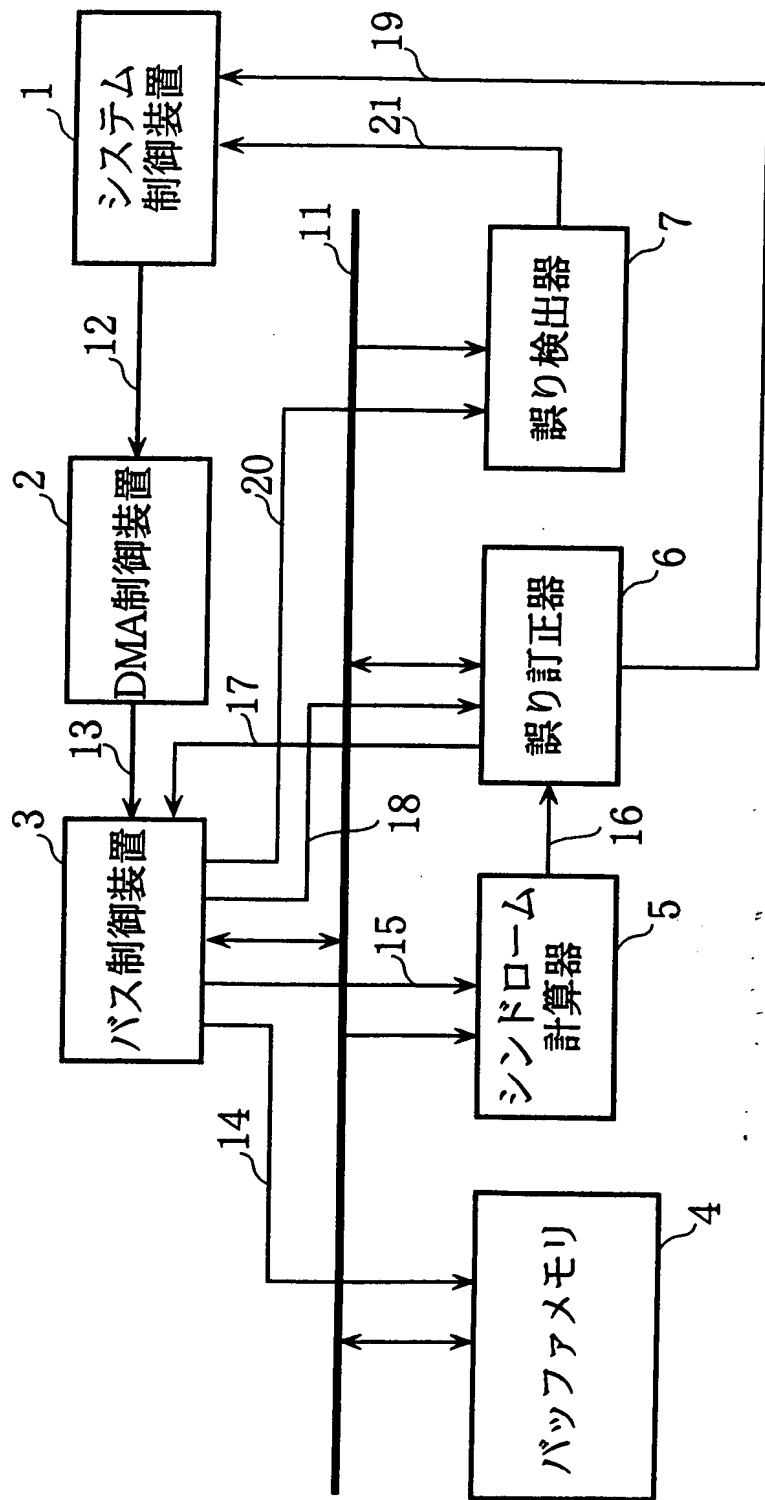


Fig. 4

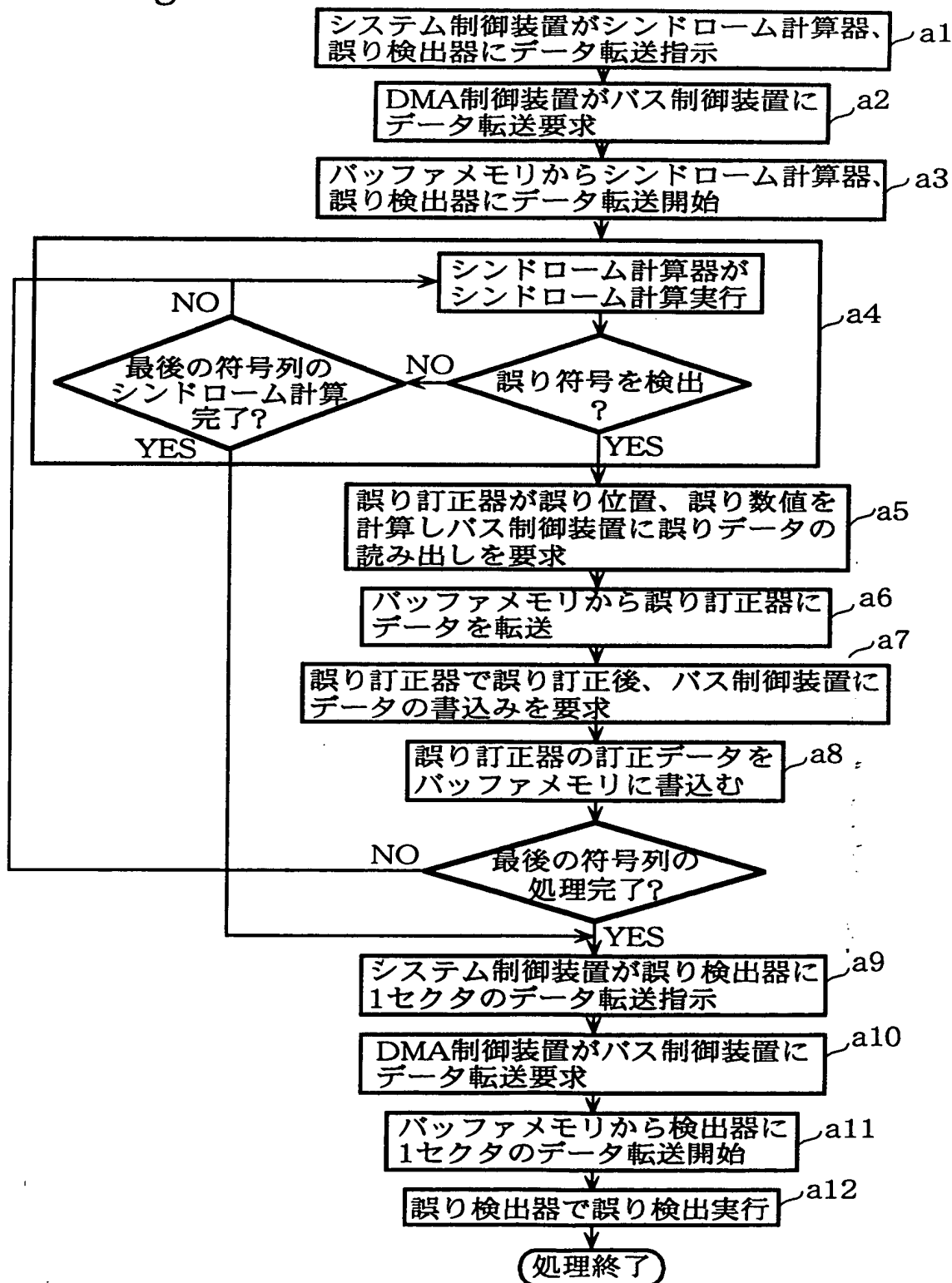
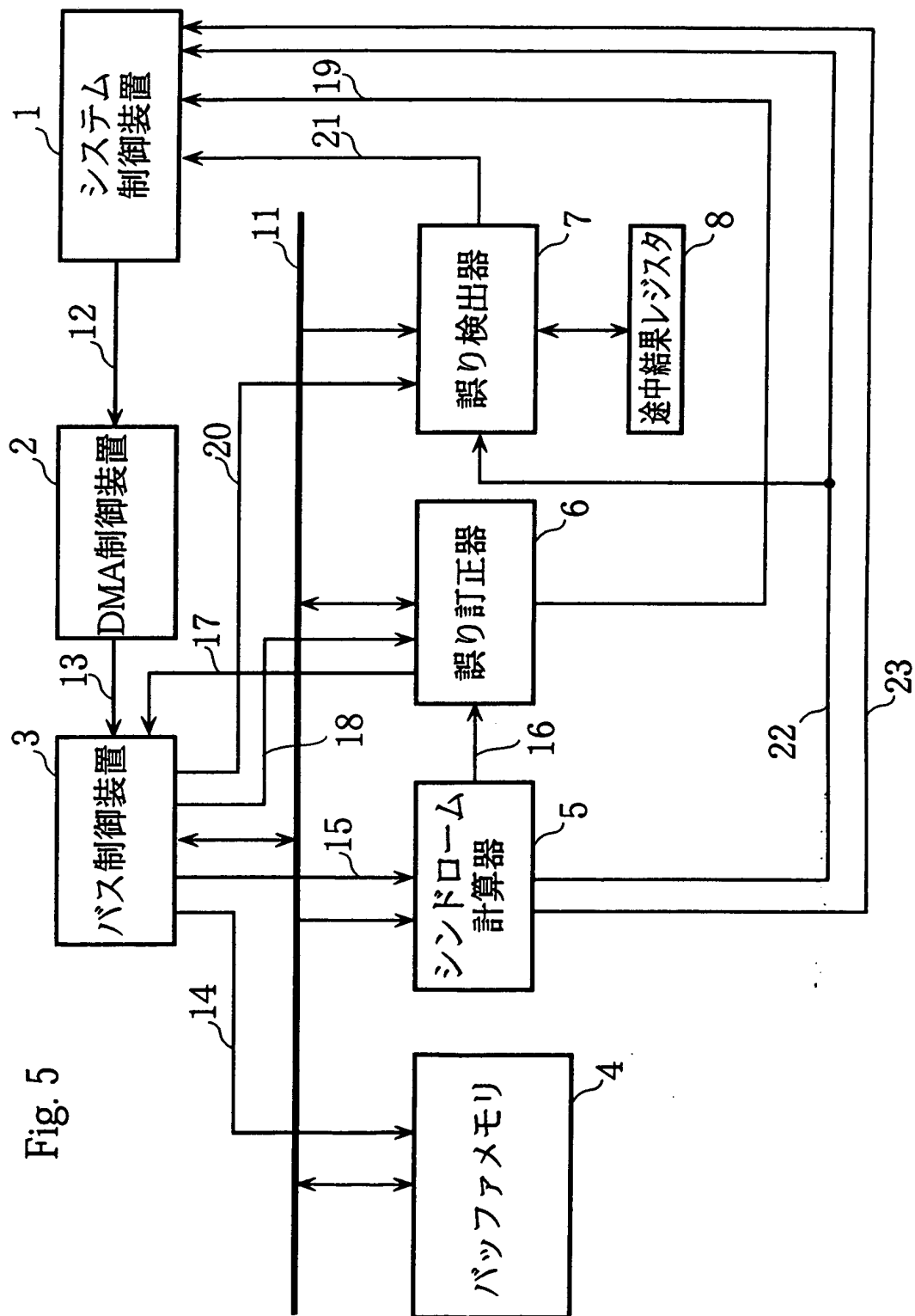


Fig. 5



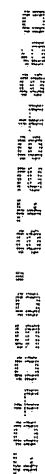
[illegible]

Fig. 7

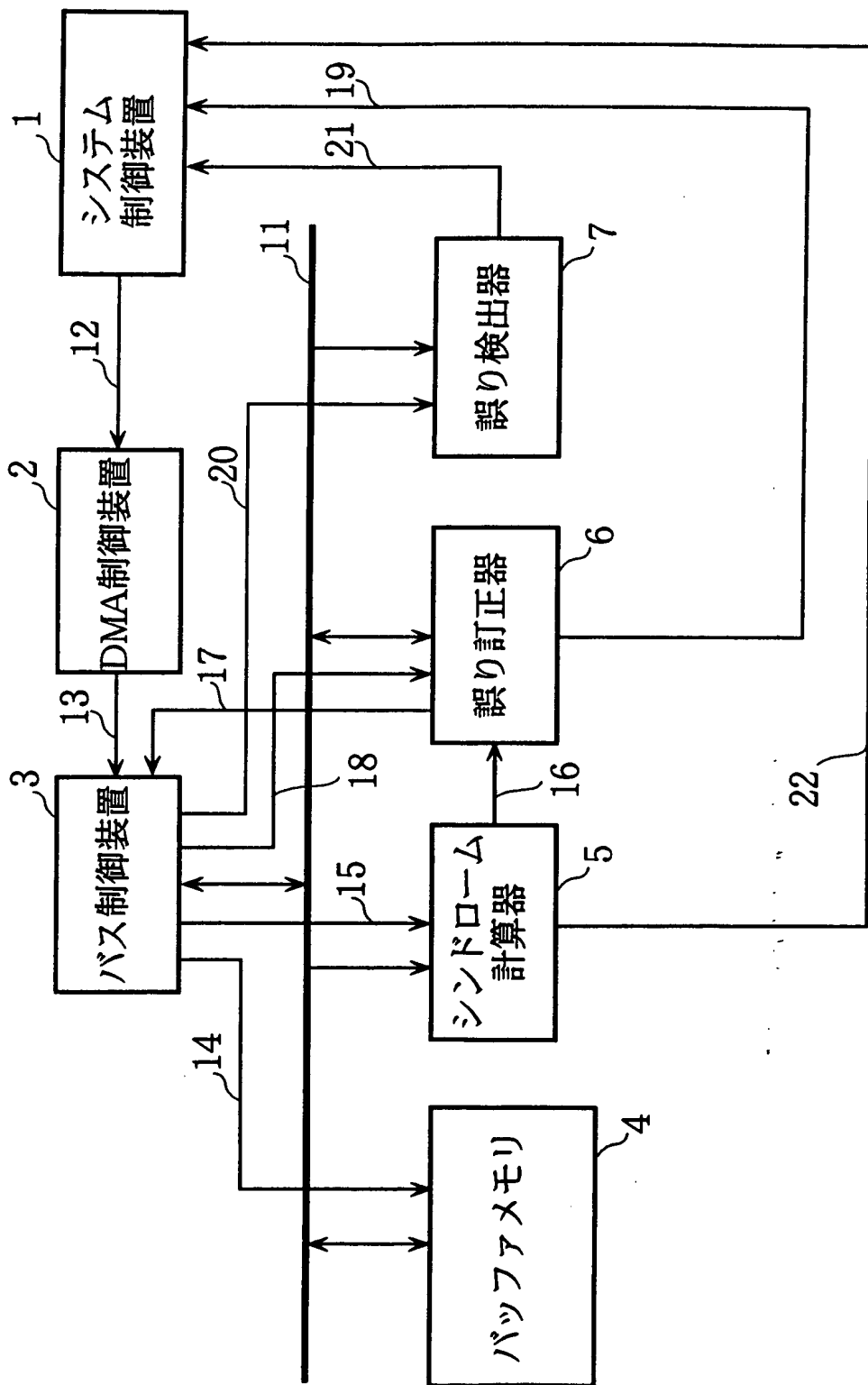
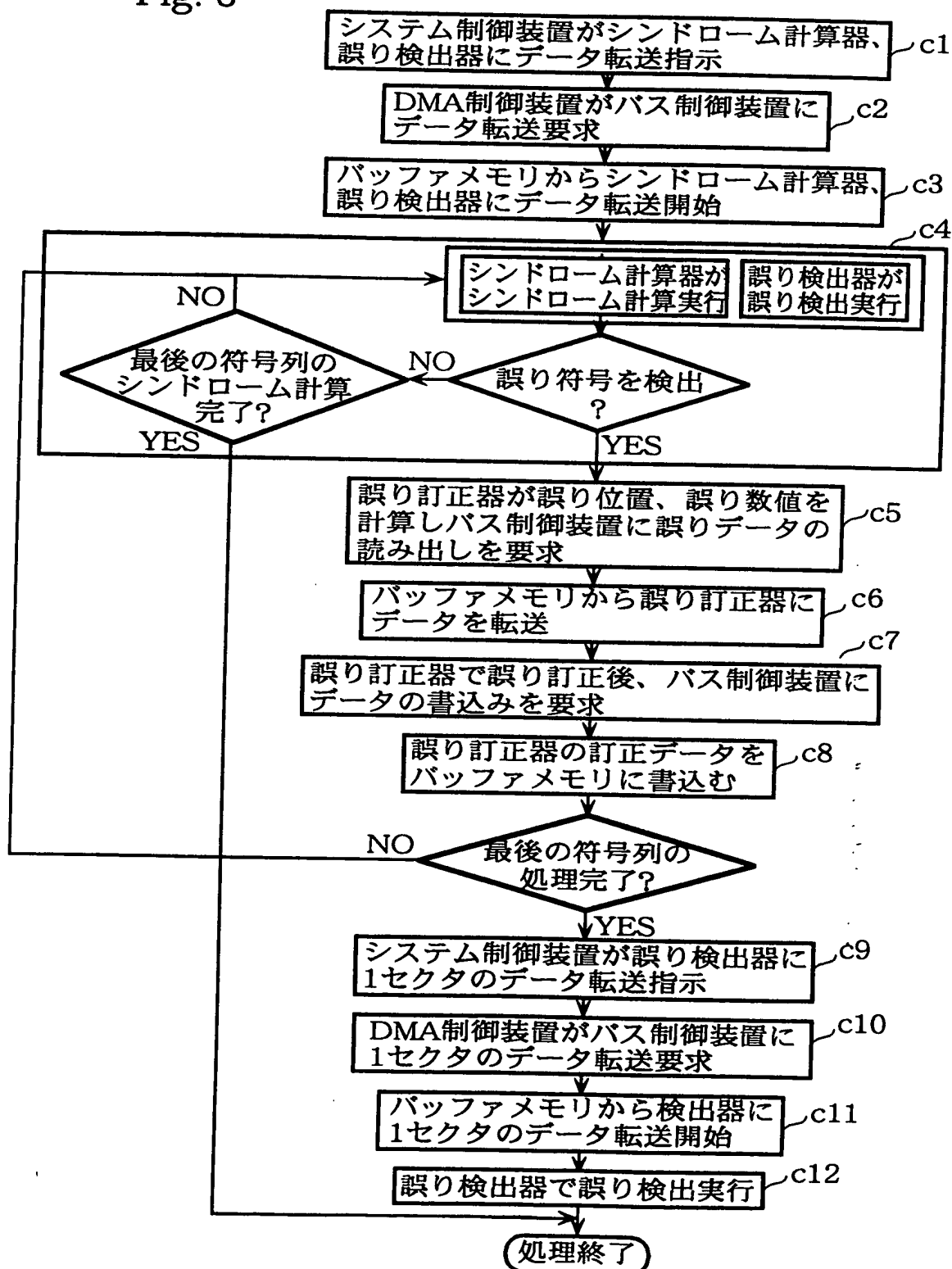


Fig. 8



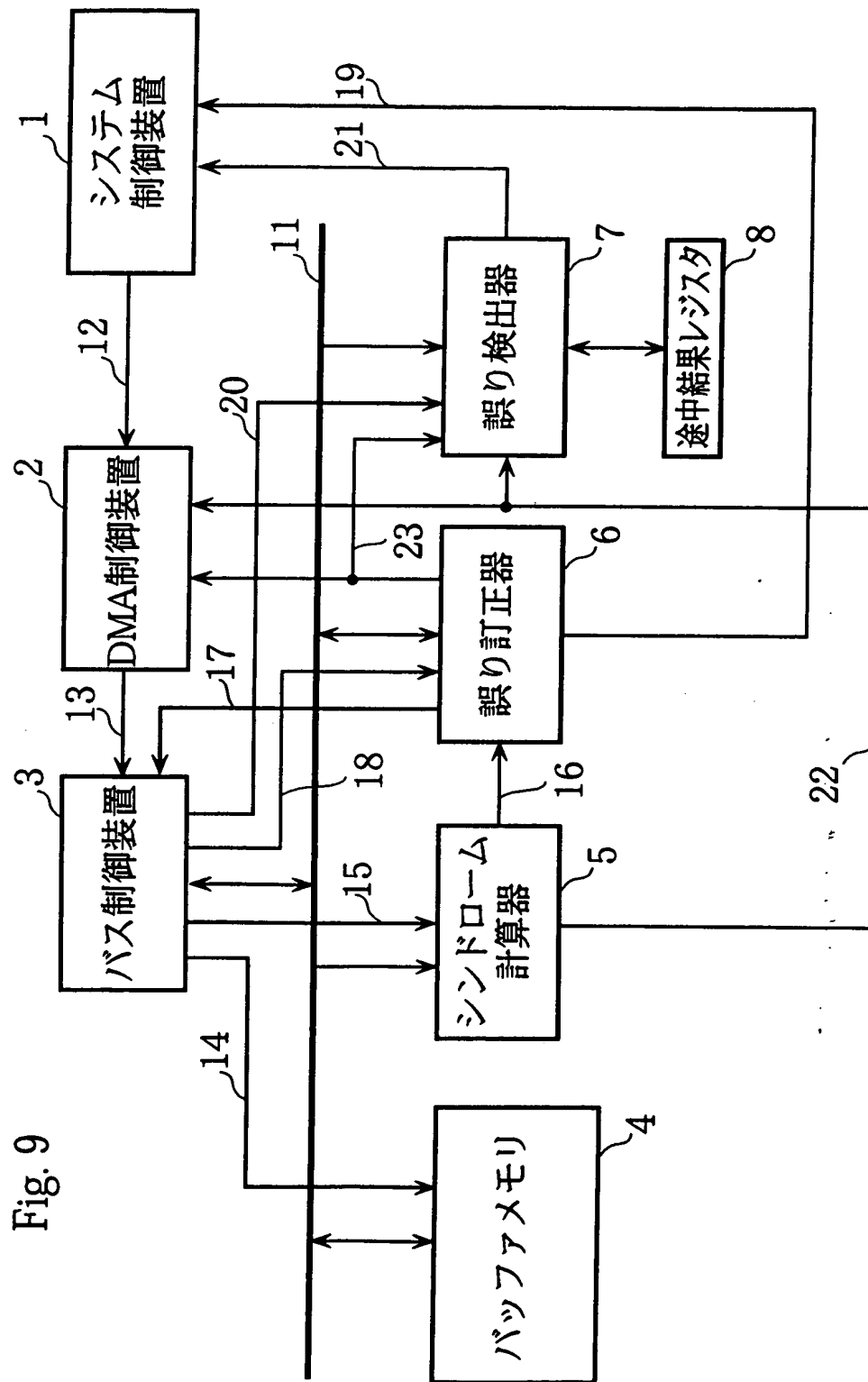


Fig. 9

Fig. 10

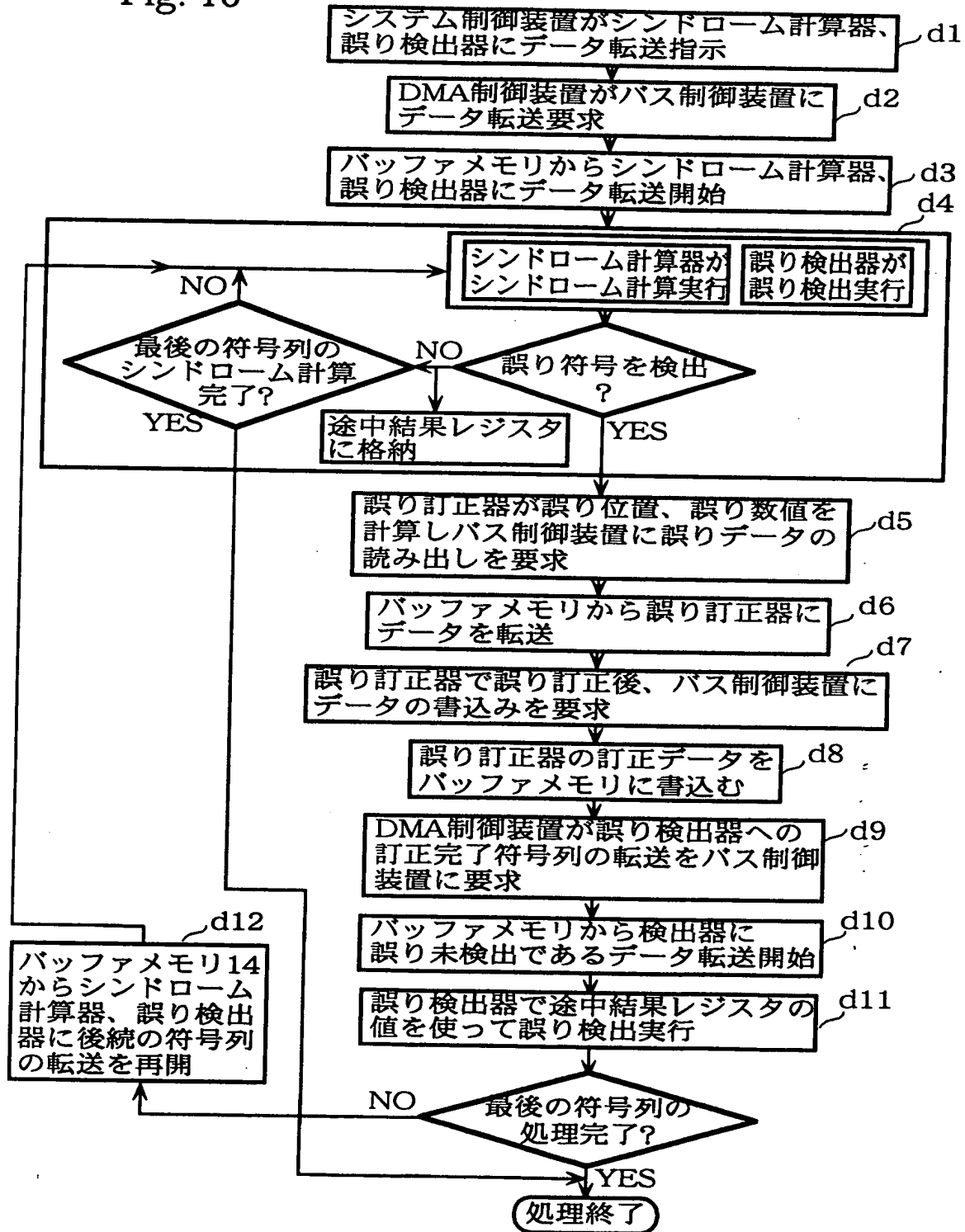


Fig. 11

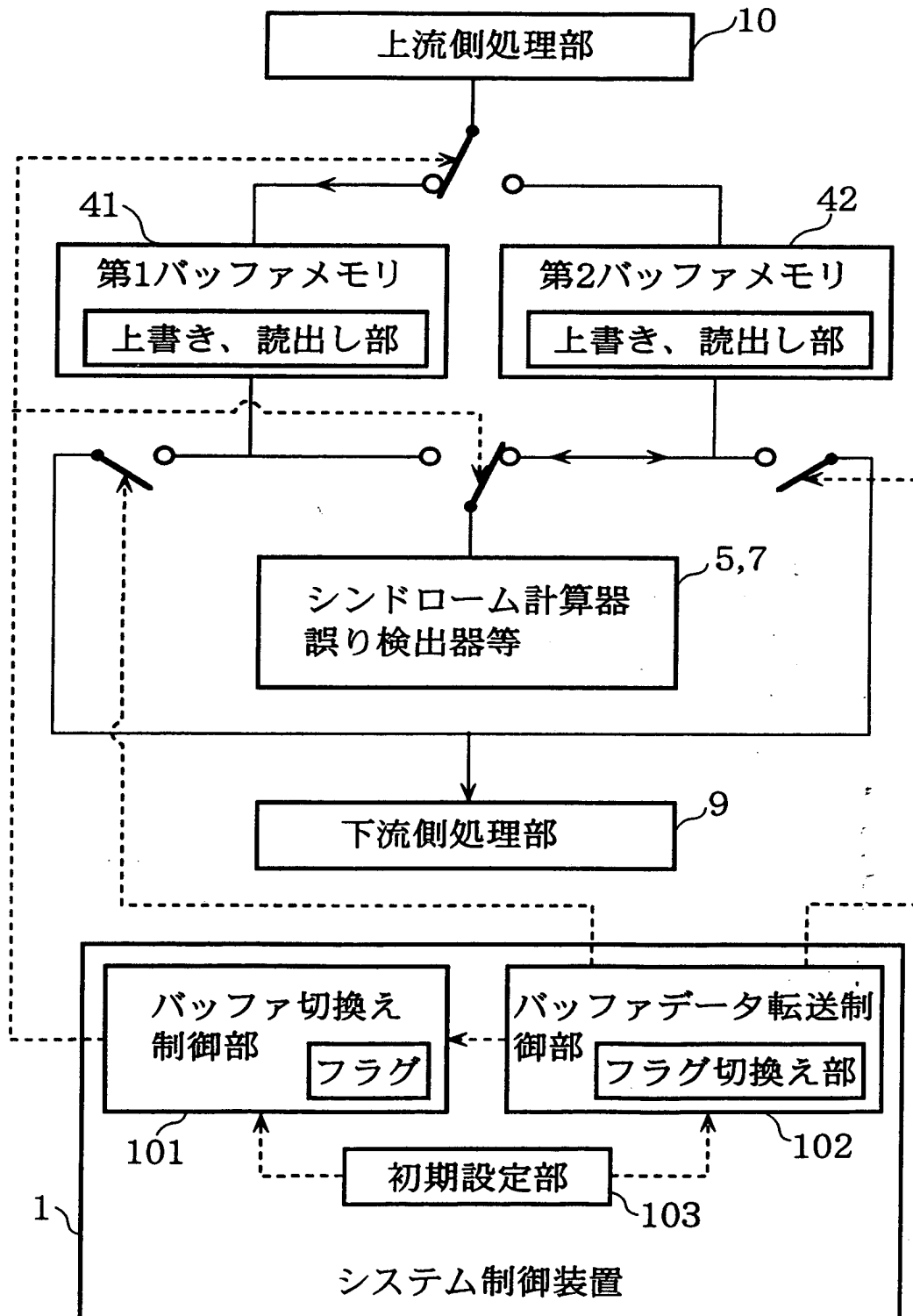


Fig. 12

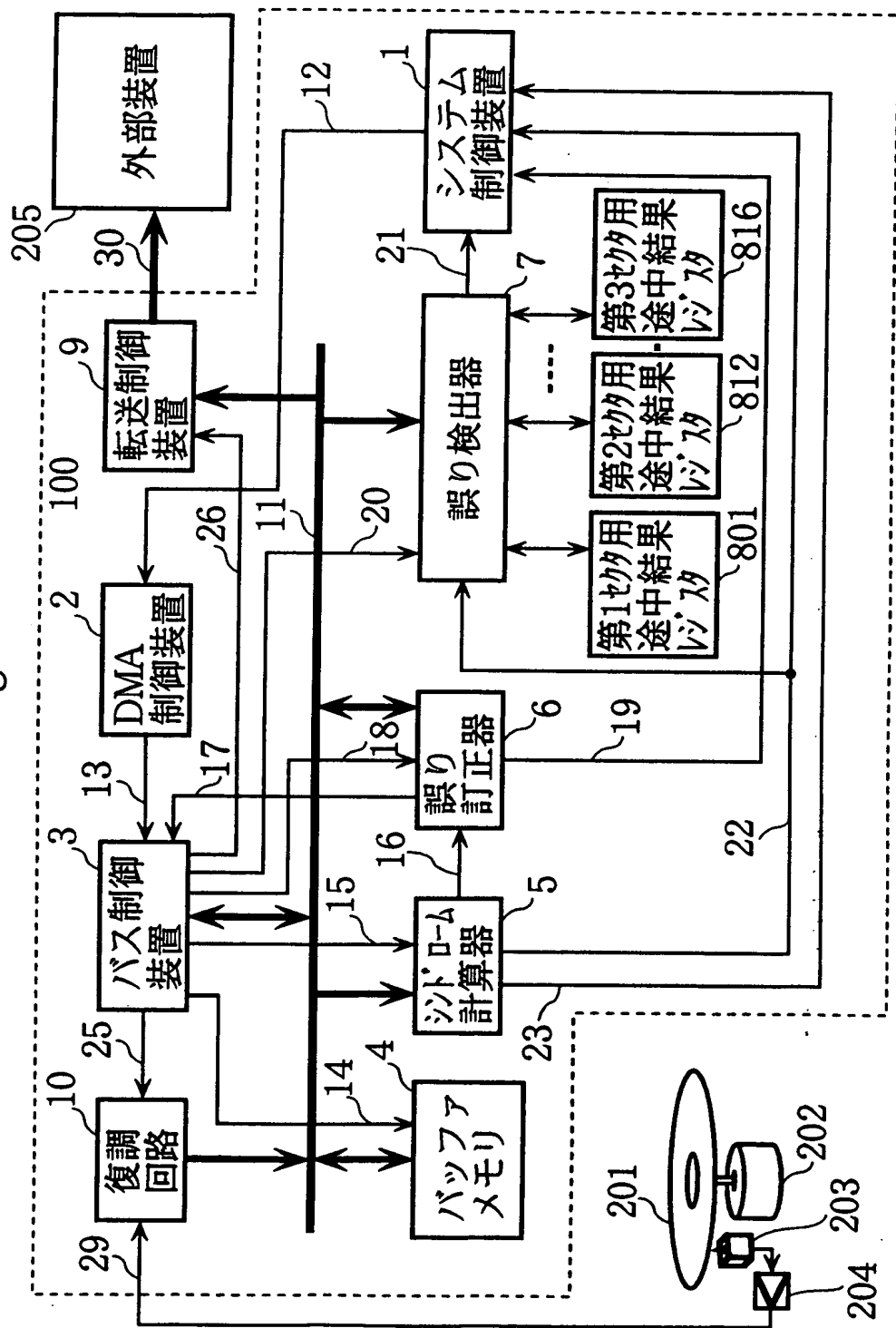


Fig. 14

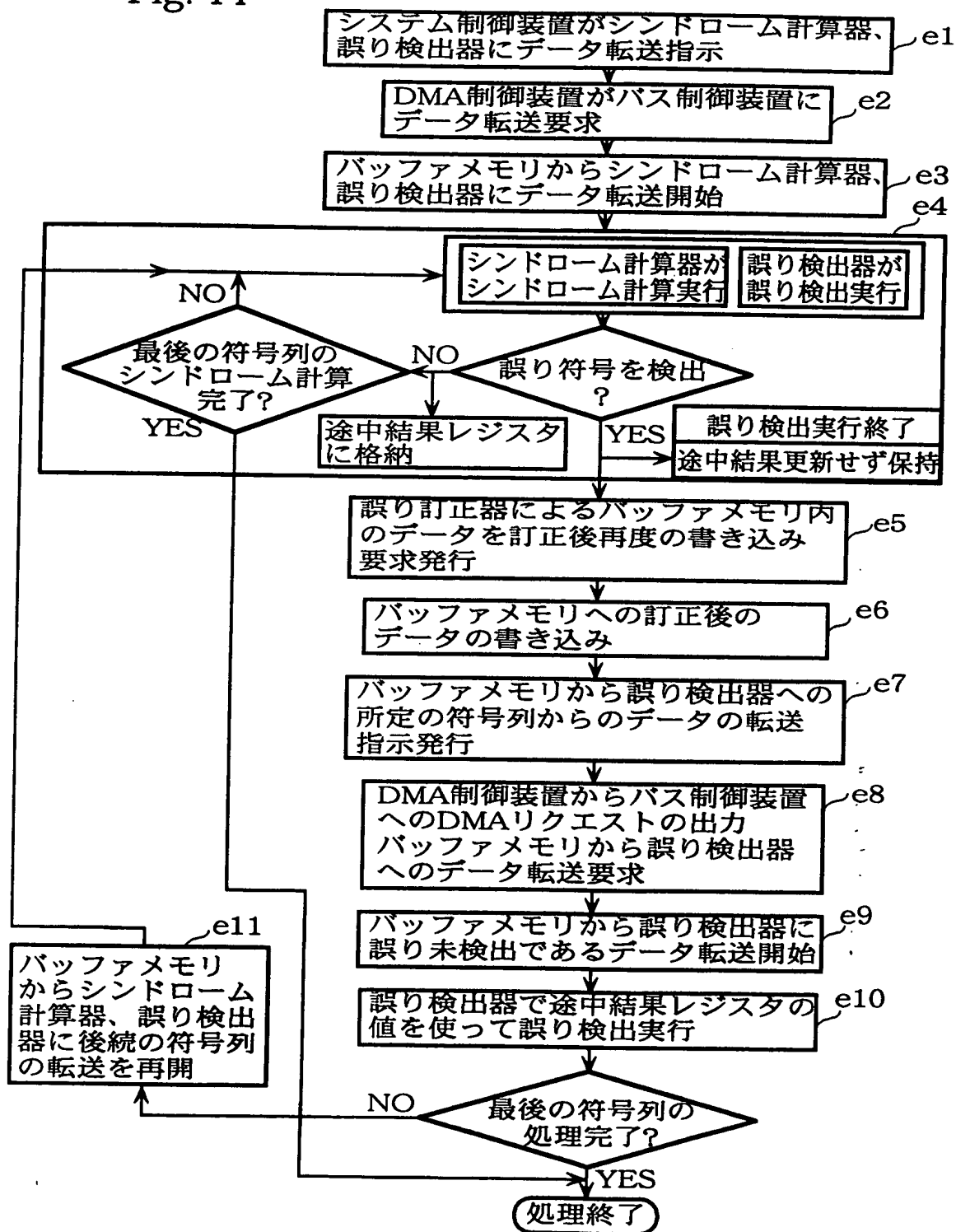


Fig. 15

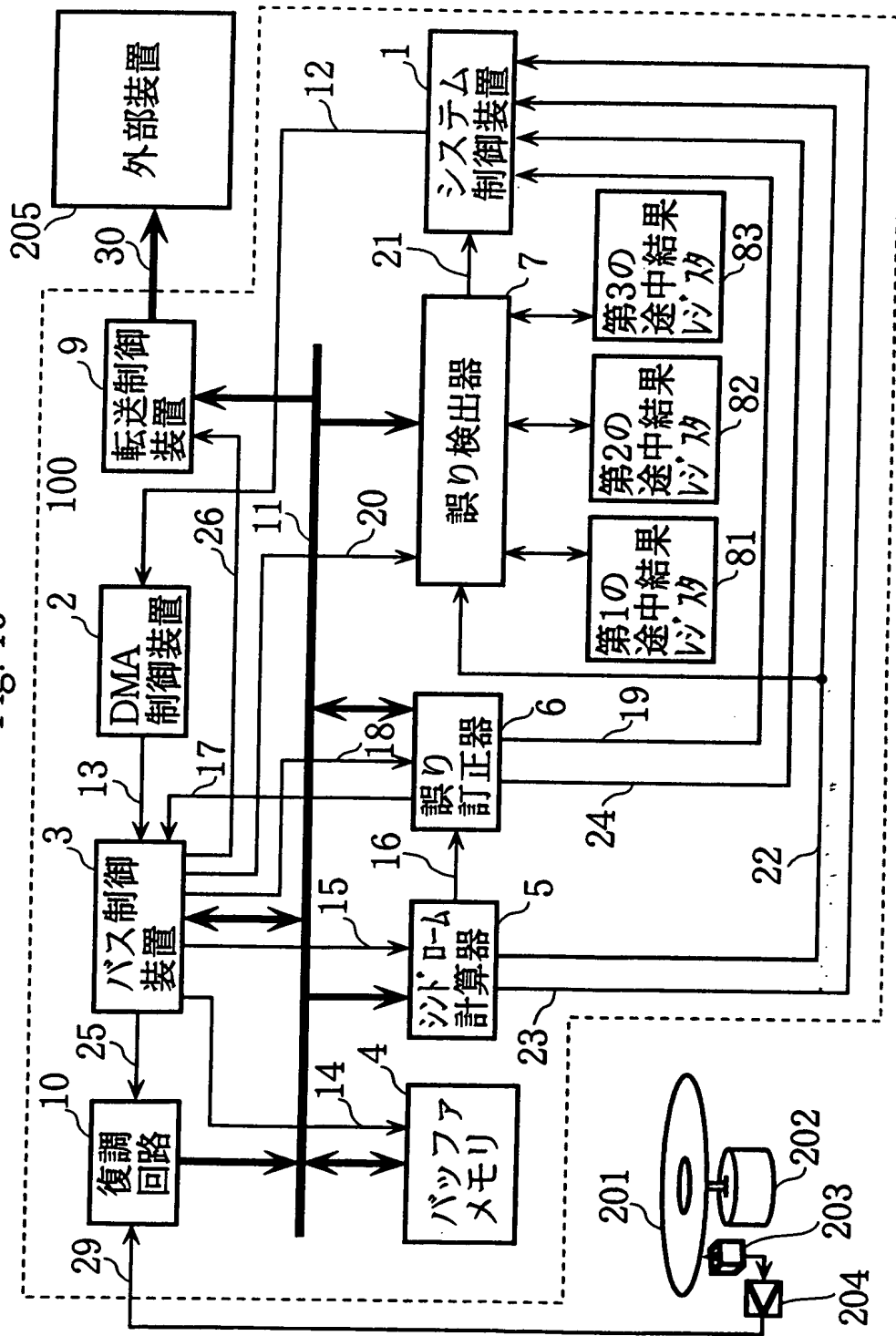


Fig. 16

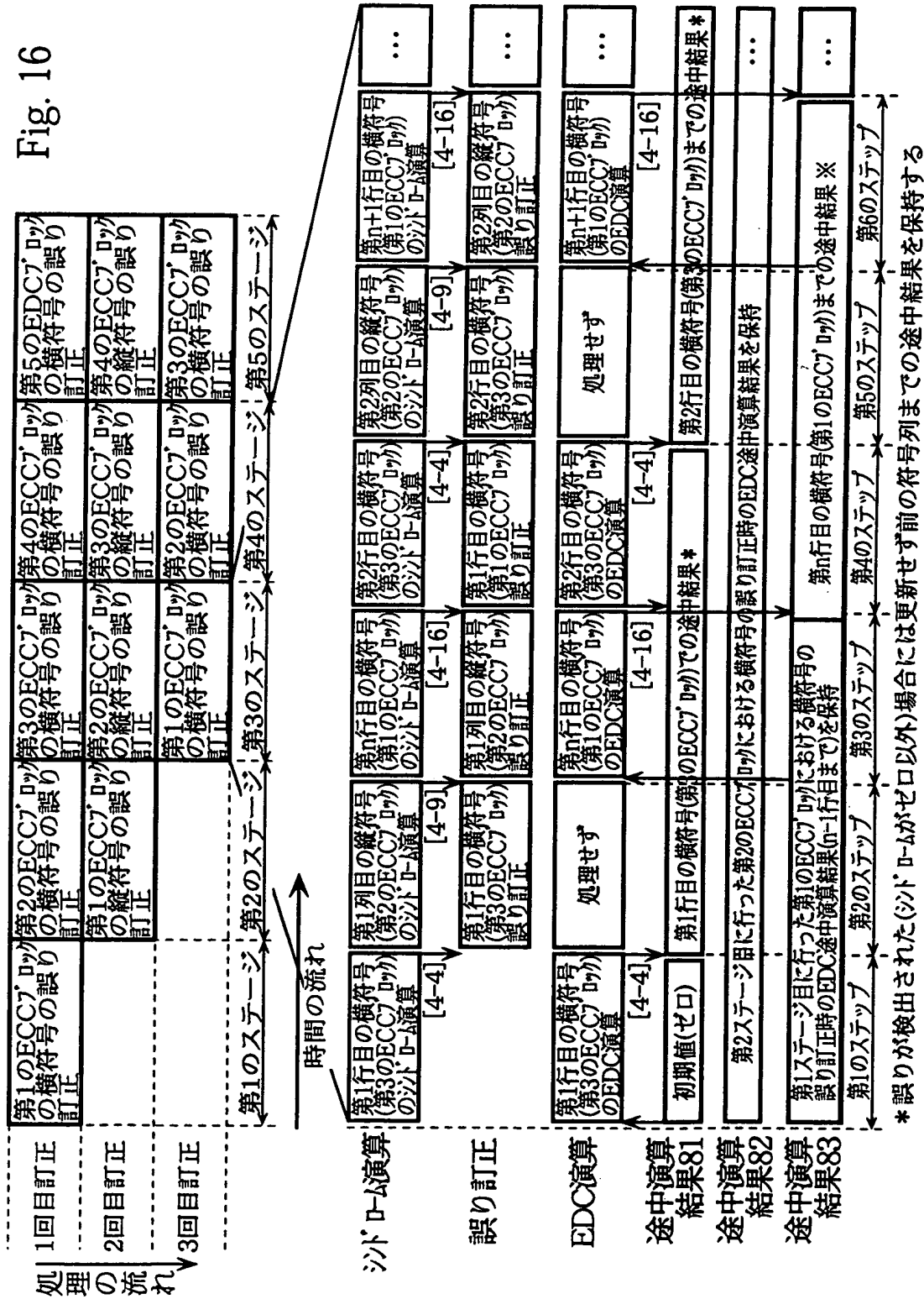
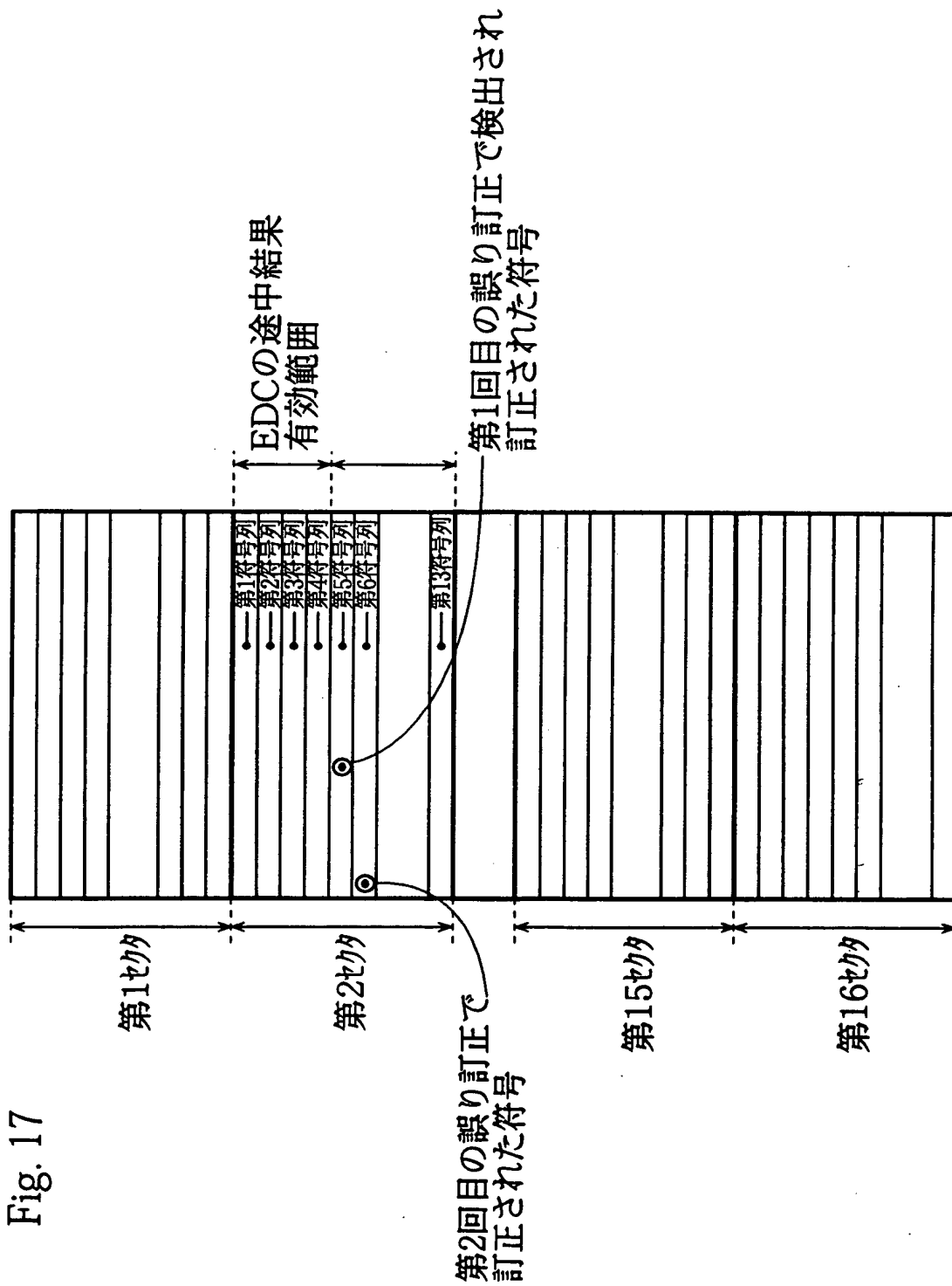


Fig. 17



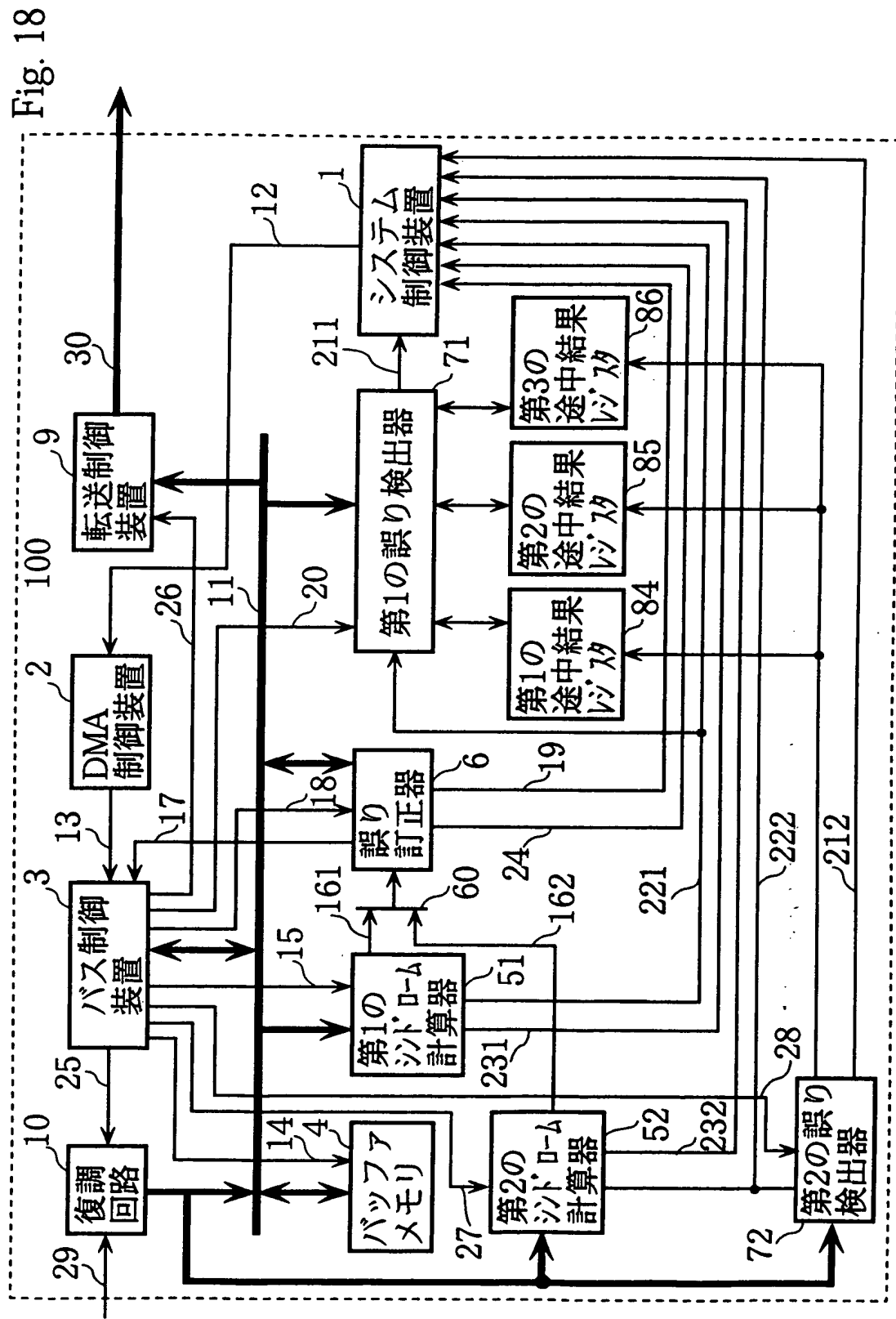


Fig. 18

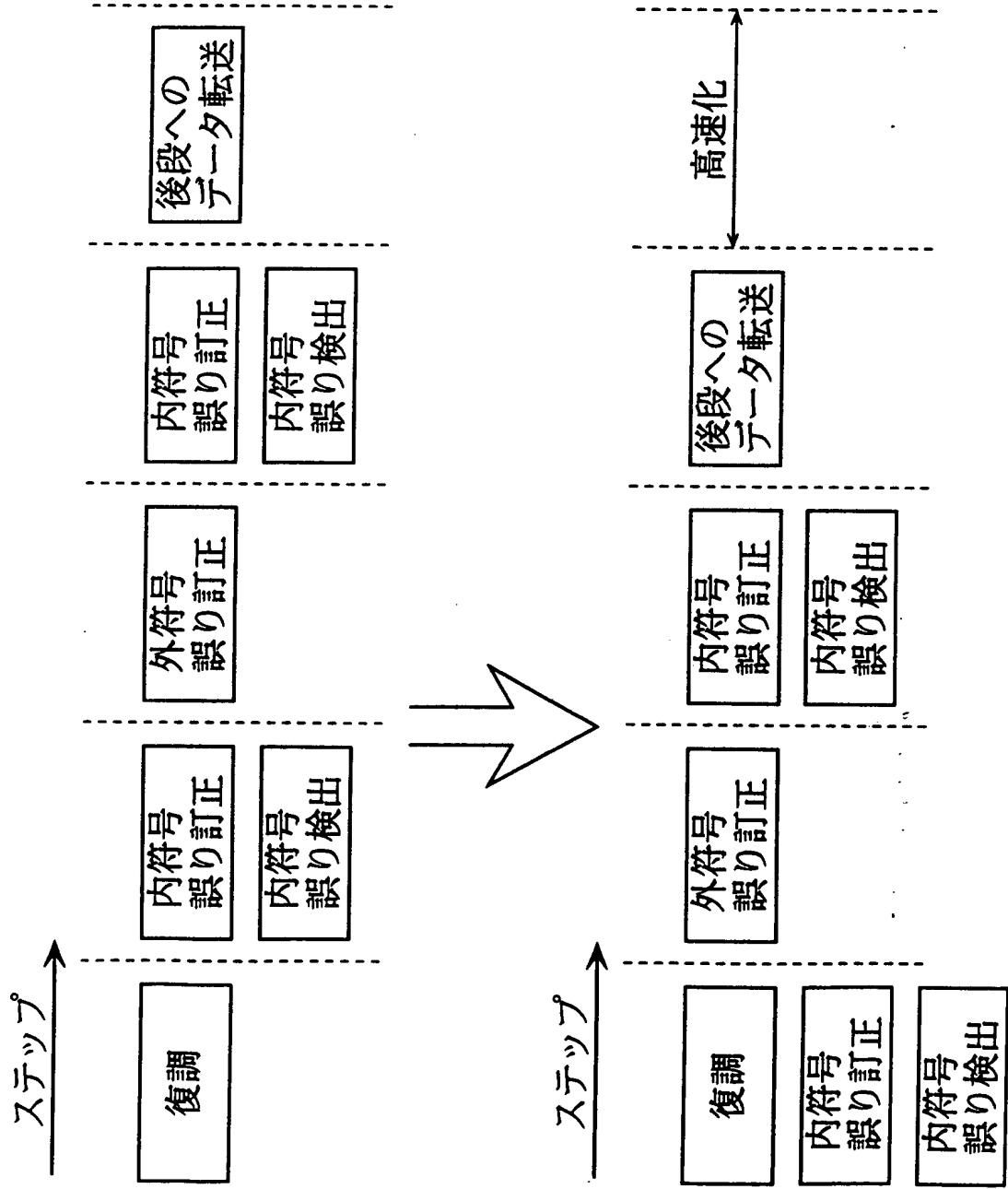


Fig. 19

Fig. 20

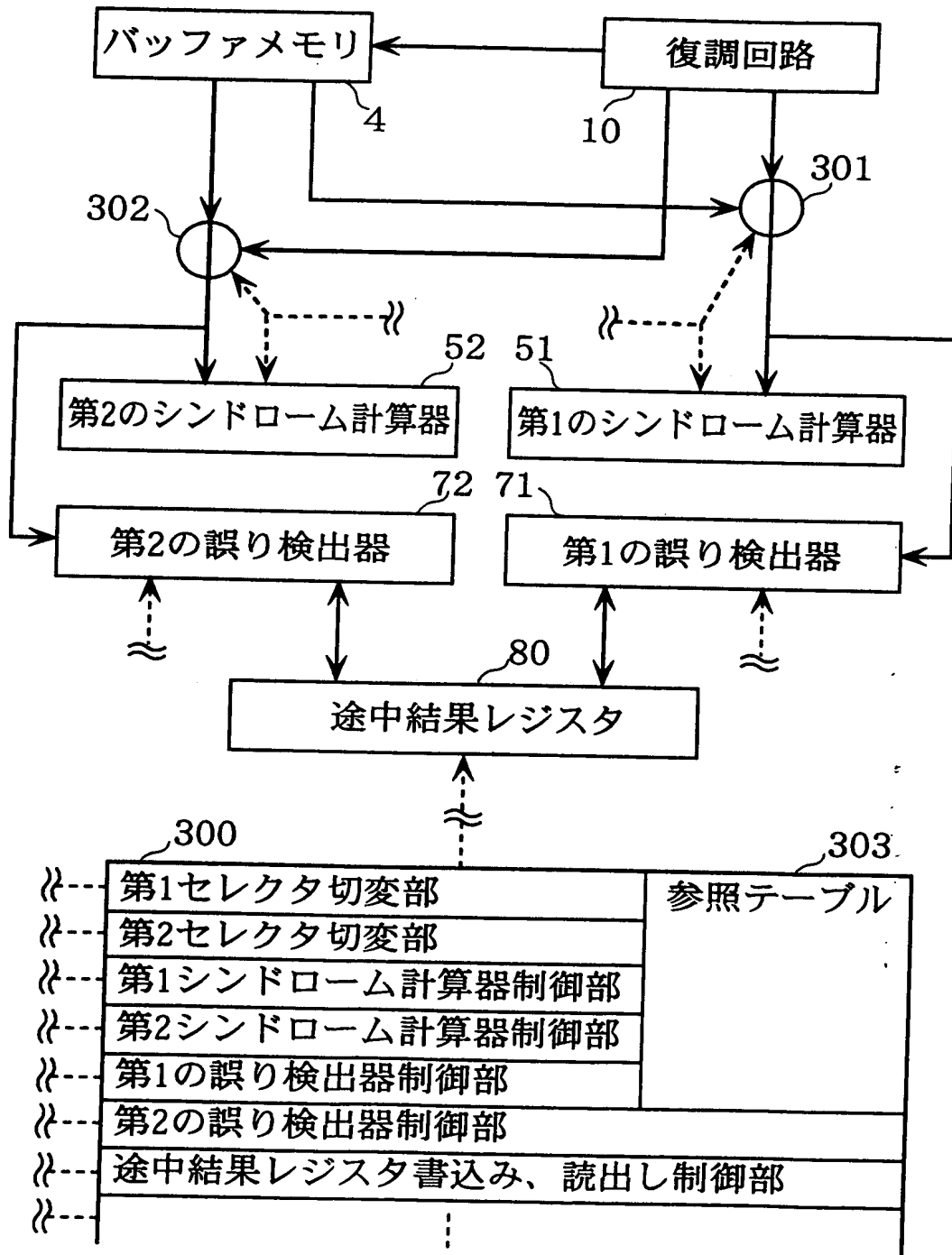


Fig. 21A

処理部	処理対象			
	ECC	セクタ	列	縦
第1シンドローム計算器	1	2	3	—
〃		〃	〃	〃
第1の誤り検出器	—	—		13
〃	〃	〃	〃	
〃	〃	〃	〃	〃

Fig. 21B

ECC	場所	セクタ	列	縦	回数
第1ECC	復調中	6	2	3	1
第2ECC	バッファメモリ	9	2	3	2
〃	バッファメモリ	〃	〃	〃	〃